



Express Mailing Label No.: EV525475213US

PATENT APPLICATION

Docket No. 14993.13

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)
)
 Joseph Walsh et al.)
)
 Serial No.: 10/615,440) Art Unit
) 2817
 Filed: July 7, 2003)
)
 Conf. No.: 7416)
)
 For: DUAL DIFFERENTIAL AMPLIFIER HAVING)
 WIDE INPUT RANGE)
)
 Examiner: Steven J. Mottola)
)
 Customer No.: 022913)

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please find, pursuant to 37 C.F.R. § 1.98(a)(1), the enclosed Form PTO-1449 which contains a list of all patents, publications, or other items that have come to the attention of one or more of the individuals designated in 37 C.F.R. § 1.56(c). While no representation is made that any of these references may be "prior art" within the meaning of that term under 35 U.S.C. §§ 102 or 103, the enclosed list of references is disclosed so as to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56.

Moreover, while no representation is made that a specific search of office files or patent office records has been conducted or that no better art exists, the undersigned attorney of record

believes that the enclosed art is the closest to the claimed invention (taken in its entirety) of which the undersigned is presently aware, and no art which is closer to the claimed invention (taken in its entirety) has been knowingly withheld.

In accordance with 37 C.F.R. §§ 1.97 and 1.98, a copy of each of the listed references or relevant portion thereof is also enclosed.

Since all listed references are either in the English language or are accompanied by a translation into English, no concise explanation of relevance is required under 37 C.F.R. § 1.98(a)(3).

((Promptness Certification or Submission Fee)
Under 37 C.F.R. § 1.97(c))

In accordance with 37 C.F.R. § 1.97(c), a PTO 2038 Credit Card form in the amount of \$180.00 is enclosed to secure consideration of the references submitted with this Information Disclosure Statement.) Please credit any over payment or charge any additional fees to Deposit Account No. 23-3178 of the undersigned.

DATED October 1, 2004.

Respectfully submitted,



Adrian J. Lee
Attorney for Applicant
Registration No. 42,785
Customer No. 022913

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) 14993.13		Application Number 10/615,440	
				Applicant(s) Joseph Walsh et al.			
				Filing Date July 7, 2003		Group Art Unit 2817	

O I P E
 OCT 01 2004
 PATENT & TRADEMARK OFFICE

U.S. PATENT DOCUMENTS							
EXAMINER	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS			(Including Author, Title, Date, Pertinent Pages, Etc.)
1		ISBN 0-7803-3416-7 Baker, R. Jacob CMOS circuit design, layout, and simulation / R. Jacob Baker, Harry W. Li, and David E. Boyce (IEEE Press series on microelectronic systems) 1997	

EXAMINER	DATE CONSIDERED
----------	-----------------

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.